



Core Information Model (CoreModel)

TR-512.A.1

Appendix Overview

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Important note

This Technical Recommendations has been approved by the Project TST, but has not been approved by the ONF board. This Technical Recommendation is an update to a previously released TR specification, but it has been approved under the ONF publishing guidelines for 'Informational' publications that allow Project technical steering teams (TSTs) to authorize publication of Informational documents. The designation of '-info' at the end of the document ID also reflects that the project team (not the ONF board) approved this TR.

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Document History

Version	Date	Description of Change
		Appendix material was not published prior to Version 1.3
1.3	September 2017	Version 1.3 [Published via wiki only]
1.3.1	January 2018	Addition of text related to approval status.
1.4	November 2018	Addition of further description and text on new appendix documents.

1 Introduction

This document is an appendix of the addendum to the TR-512 ONF Core Information Model and forms part of the description of the ONF-CIM. For general overview material and references to the other parts refer to [TR-512.1](#).

1.1 References

For a full list of references see [TR-512.1](#).

1.2 Definitions

For a full list of definition see [TR-512.1](#).

1.3 Conventions

See [TR-512.1](#) for an explanation of:

- UML conventions
- Lifecycle Stereotypes
- Diagram symbol set

1.4 Viewing UML diagrams

Some of the UML diagrams are very dense. To view them either zoom (sometimes to 400%) or open the associated image file (and zoom appropriately) or open the corresponding UML diagram via Papyrus (for each figure with a UML diagram the UML model diagram name is provided under the figure or within the figure).

1.5 Understanding the figures

Figures showing fragments of the model using standard UML symbols and also figures illustrating application of the model are provided throughout this document. Many of the application-oriented figures also provide UML class diagrams for the corresponding model fragments (see [TR-512.1](#) for diagram symbol sets). All UML diagrams depict a subset of the relationships between the classes, such as inheritance (i.e. specialization), association relationships (such as aggregation and composition), and conditional features or capabilities. Some UML diagrams also show further details of the individual classes, such as their attributes and the data types used by the attributes.

2 Introduction to the Appendix

This section provides an overview of the TR-512.A.x appendix documents (the "A series"). Each appendix document described has a hyperlink that will take you to the document in your system¹. The documents are all in the "ModelDescriptions" folder.

¹ The link will only work if you have unzipped the whole package as one.

These appendix documents provide descriptive support for the CIM by explaining the rationale behind the model and giving examples. The Appendix documents are summarized in the following sections.

2.1 Model Structural Patterns and Architecture ([TR-512.A.2](#))

This document explains the model patterns and architecture that underpin the CIM. It:

- Works through the key patterns in the model such as hypergraph and component-system.
- Discusses the fundamentals of transport and how these are described in terms of the patterns
- Shows some high level examples of the patterns
- Explains how the patterns can be intertwined to form the architecture of the network from a control perspective

The descriptions in this document are built from descriptions in earlier referenced works.

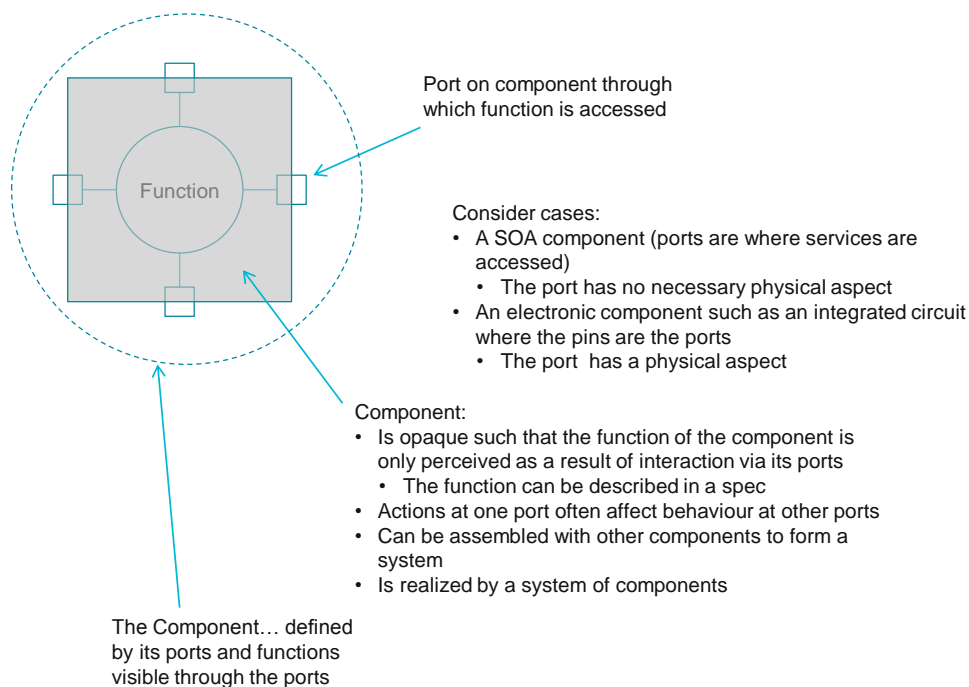
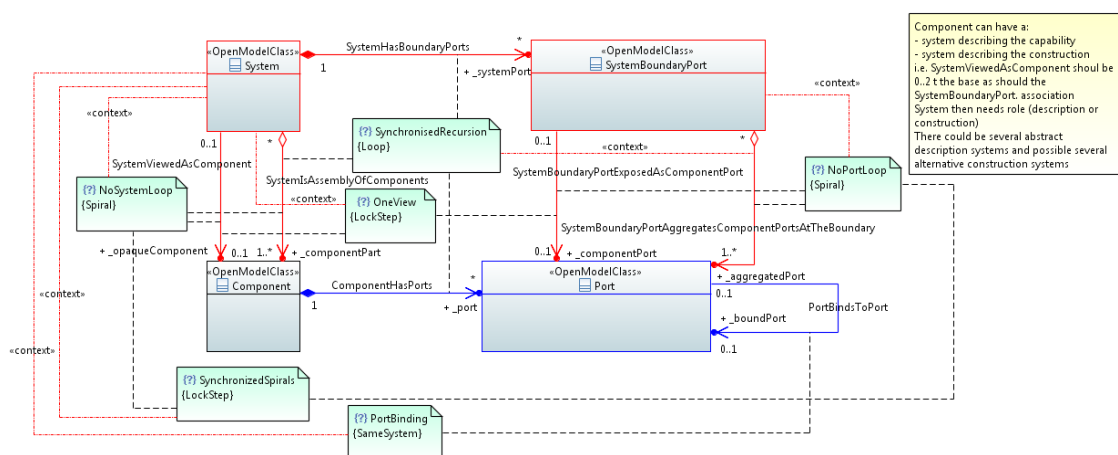


Figure 2-1 The Component

In TR-512 V1.4, the Component-system pattern has been further defined.

The following figure shows the essential structure of the Component – System pattern. The essence of Component – Port remains as above, but the System has gained explicit boundary ports. This enhanced model form better enables the recursion from opaque component through System surface to System detail.



CoreModel diagram: Patterns-ComponentSystemPattern

Figure 2-2 The Component – System Pattern

2.2 Rationale Behind the CIM ([TR-512.A.3](#))

This document provides the drivers for the CIM work and highlights benefits of the work to the industry.

The document explains that management of networks and devices today is a complex operational challenge resulting from, and exacerbated by, the plethora of conflicting standards and incompatible implementations. Almost all of the existing models use inconsistent terminology and outdated concepts that aren't applicable to SDN/NFV scenarios.

The document goes on to explain that the ONF Information Modeling team:

- Has defined a consistent set of fundamental concepts and the relationship among them by leveraging the knowledge gained from years of management standards evolution and pragmatic implementation/software development experience. These concepts are capable of representing both legacy management and SDN/NFV concepts/scenarios, while allowing for consistent management in hybrid environments.
- Employs a realistic federated model with a layered model architecture and managed dependencies. This is comprised of a stable core model (which is itself modular for scalability) and technology-specific model extensions that can be added in a timely manner without destabilizing the core.

It is emphasized that the ONF Information model is not a purist, theoretical artifact, but a pragmatic one that forms part of a tooling chain, enabling context and technology specific interfaces in different languages to be generated from a key set of definitions.

The document explains the growing utilization of the ONF CIM across various bodies such as ITU-T SG15, ETSI NFV, MEF and TMF, as well as by some major service providers.

2.3 Analogue and Media (L0) examples ([TR-512.A.4](#))

This document explains the model in terms of examples applied to the analogue space of Layer 0 (L0). The document focuses on optical photonics and guides the use of the model to represent both device functions and network structures.

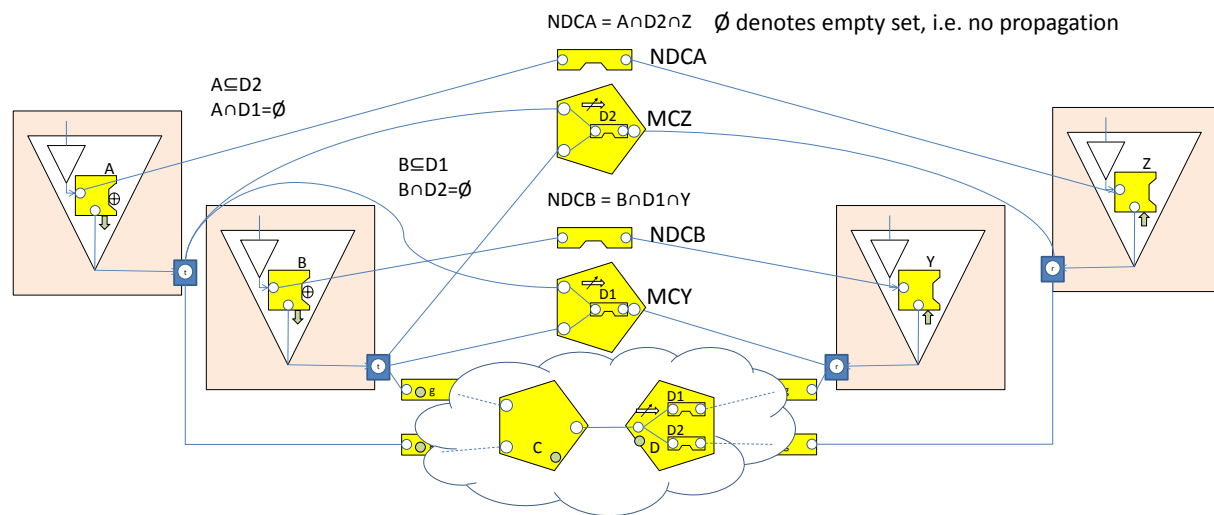


Figure 2-3 Network Domain Channel formed from Media Channels

In V1.4 overhead and monitoring were considered and this led to a bidirectional treatment of assemblies of channels.

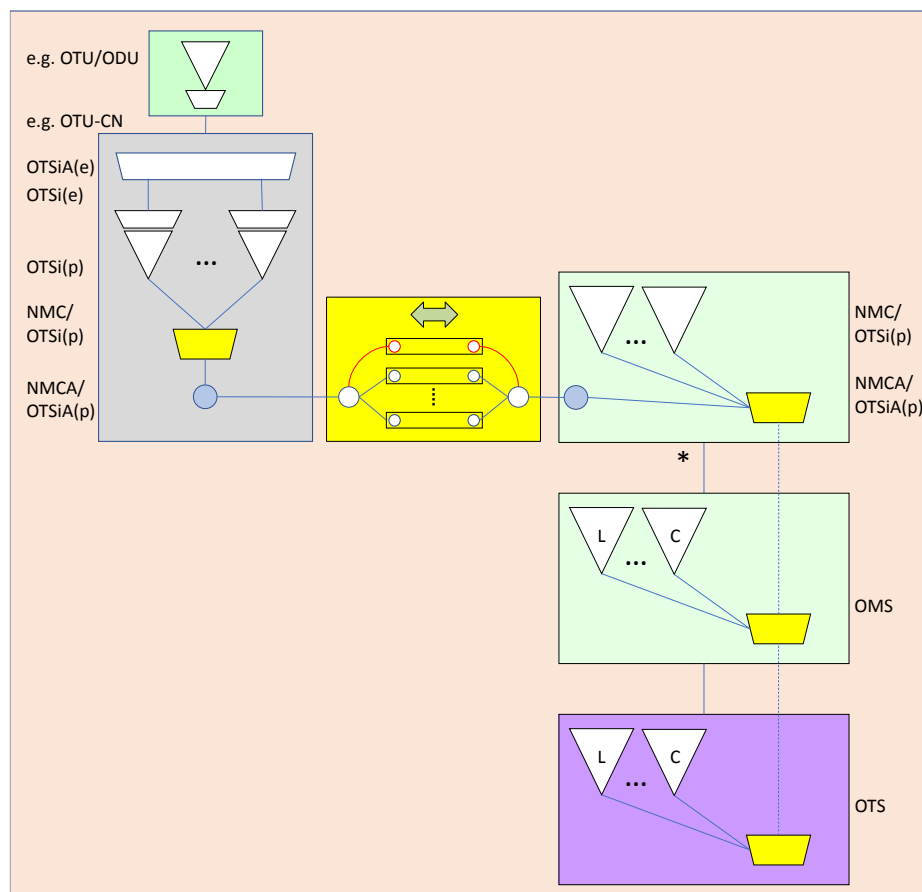


Figure 2-4 Simplified representation showing OMS/OTS, NMCA, and OTSi termination

2.4 Circuit Switched (L1 & L2) examples ([TR-512.A.5](#))

This document was added in V1.3.1. It introduces a model of layer protocols for circuit switched systems. The figures show a compact diagrammatic representation of the order of layer protocols for various port types.

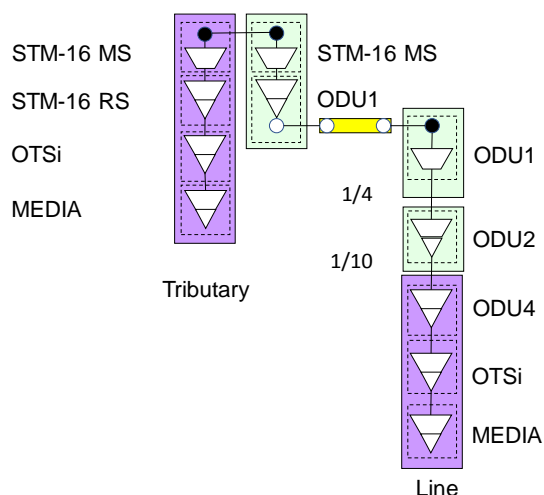


Figure 2-5 Basic OTN device example showing compact representation of a STM-16 Tributary

At this point the document provides a few basic examples. It is intended that, in subsequent releases the document will progress from basic single device circuit switched examples through to complex protection scenarios and multi-layer circuit switched examples. The document will also provide multi-layer examples where:

- Circuit switched forwarding is carried by an analogue server
- An analogue signal is carried by a circuit switched system
- There is a critical differential delay and a critical round trip delay consideration

2.5 Packet Switched (L2 & L3) examples ([TR-512.A.6](#))

The document provides a view of basic packet switched examples and then works through to complex network scenarios including multi-layer packet switched examples.

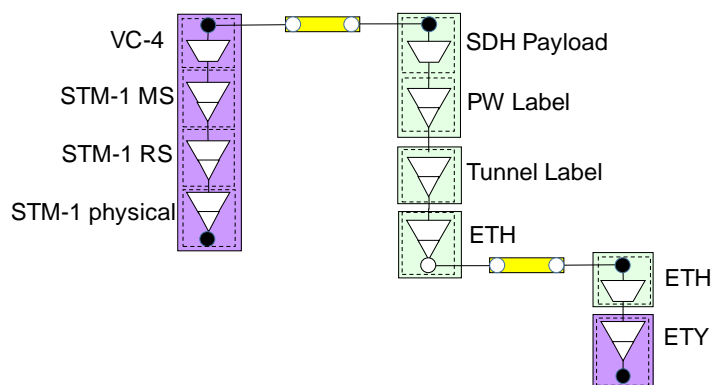


Figure 2-6 STM-1 Circuit Emulation Service over Ethernet

The document provides multi-layer examples where:

- Packet switched forwarding is carried by a circuit switched server
- Circuit switched forwarding is carried by a packet switched server

- Packet switched forwarding is carried by an analogue server.

2.6 Control and Interaction examples ([TR-512.A.7](#))

The document focusses on examples of control structures progressing from basic single device control examples through to complex peered control and multi-level control scenarios.

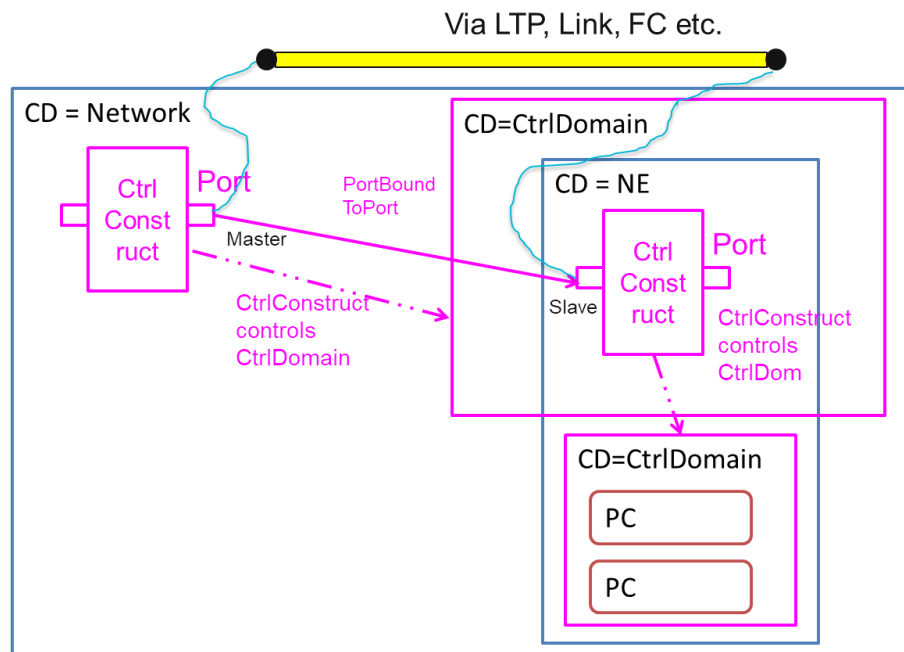
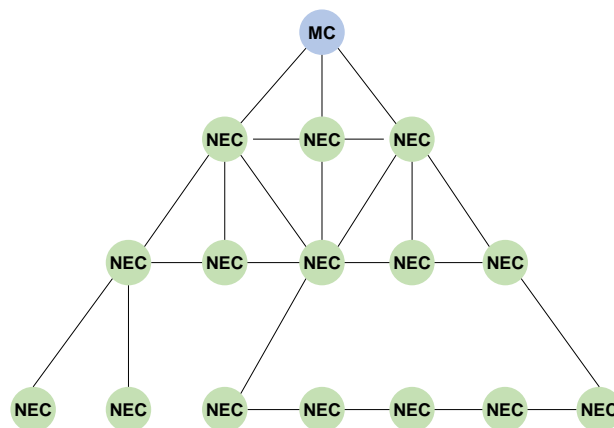


Figure 2-7 - Basic ControlConstruct layering Use Case

The document will consider interaction examples in a future release.

2.7 Timing & Synchronization examples ([TR-512.A.8](#))

This document provides a description of time and frequency synchronization in a telecommunications network and provides examples of the use of the model to represent these synchronization functions.



MC Master Clock

NEC Node that contains a Network Element Clock

— Potential synchronization trail

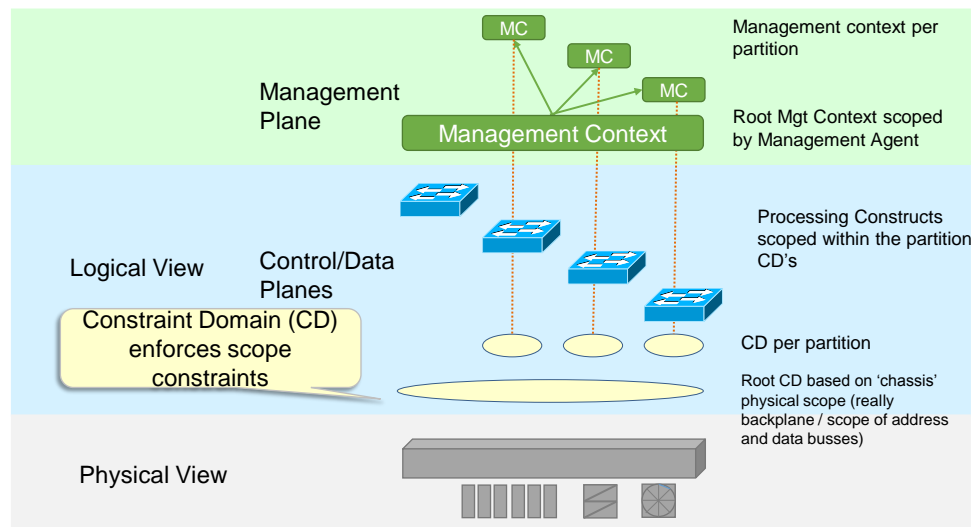
Functional model of sync distribution topology

- Frequency and time sync may use different topologies

Figure 2-8 Example synchronization distribution network

2.8 Processing Construct examples ([TR-512.A.9](#))

This document provides various examples of the use of the ProcessingConstruct model to represent complex functions.



The management plane may be global or partitioned, or both (as shown).
 Root MC, Root CD and Physical Inventory have same scope.

Figure 2-9 "Virtual Device"

2.9 Specification examples ([TR-512.A.10](#))

This document provides various examples of the use of the CIM specification model to express constraints in various real contexts.

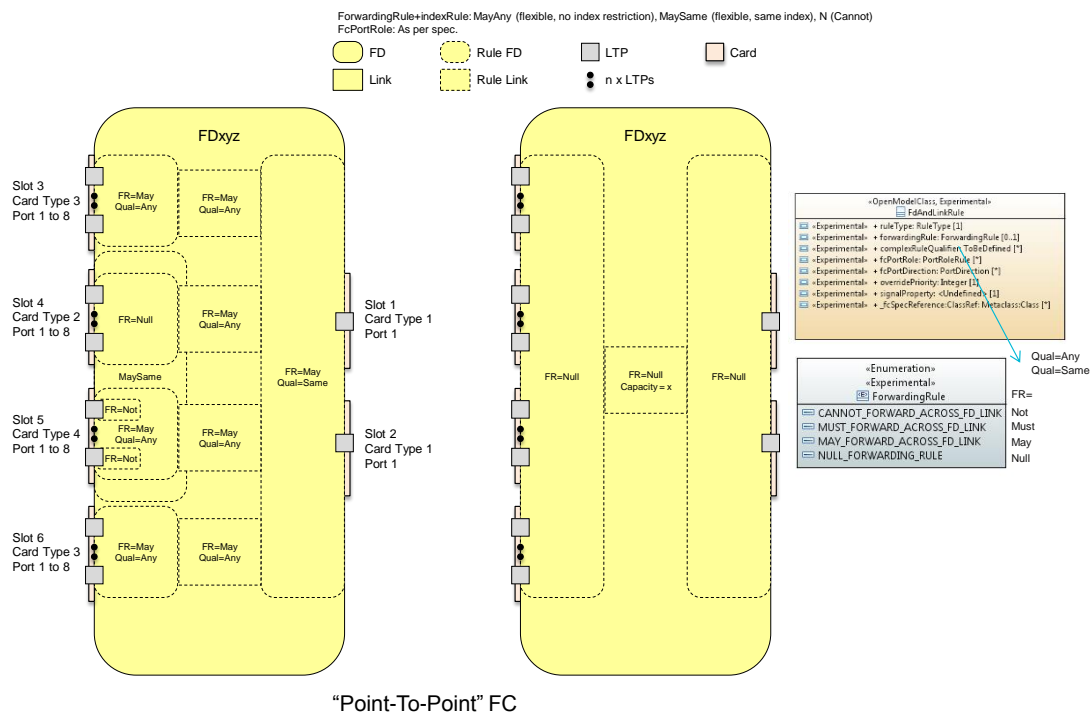


Figure 2-10 Example Use of FD Spec

2.10 Resilience examples (TR-512.A.11)

This document provides various examples of the use of the ONF CIM to represent common resilience schemes. This document is not exhaustive. The model is built from a several generalized constructs that should readily support many other protection schemes not described here.

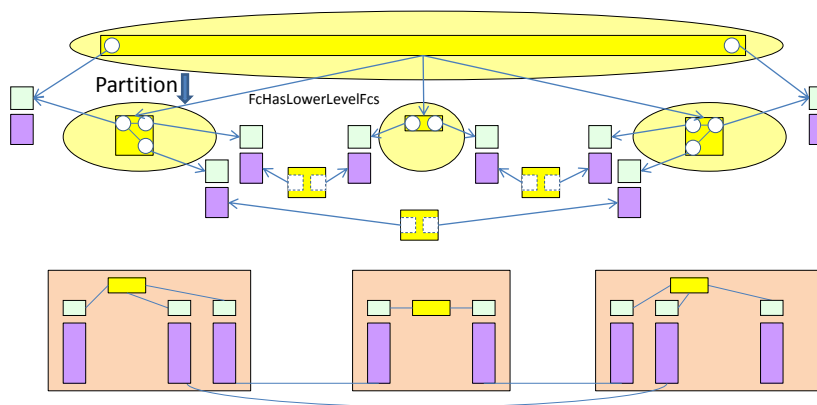


Figure 2-11 Simple summary example of 1?1 cases (represented via partition)

2.11 Application (L4 and above) examples (TR-512.A.12)

This document is not part of this release, it will be provided in a later release. The document will progress from basic simple application through to complex application assemblies.

2.12 Software examples ([TR-512.A.13](#))

This document provides examples of the use of the CIM software model starting from basic cases such as a routing process in a router as shown below.

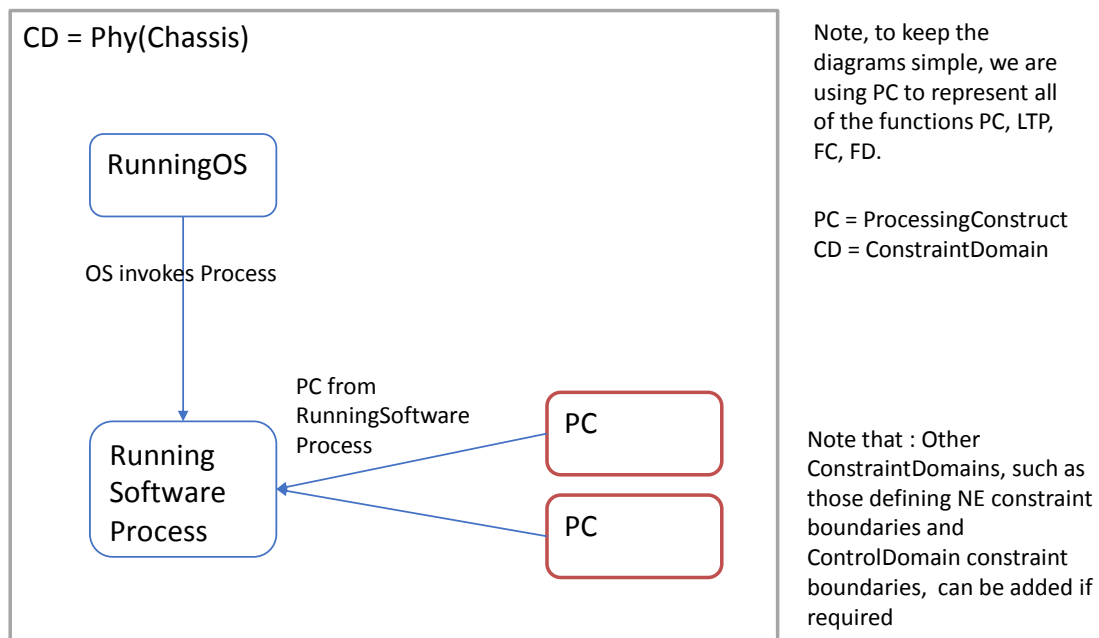


Figure 2-12 The Routing "process" on router

The document progresses from the basic examples through increasingly complex cases to sophisticated examples including details of control.

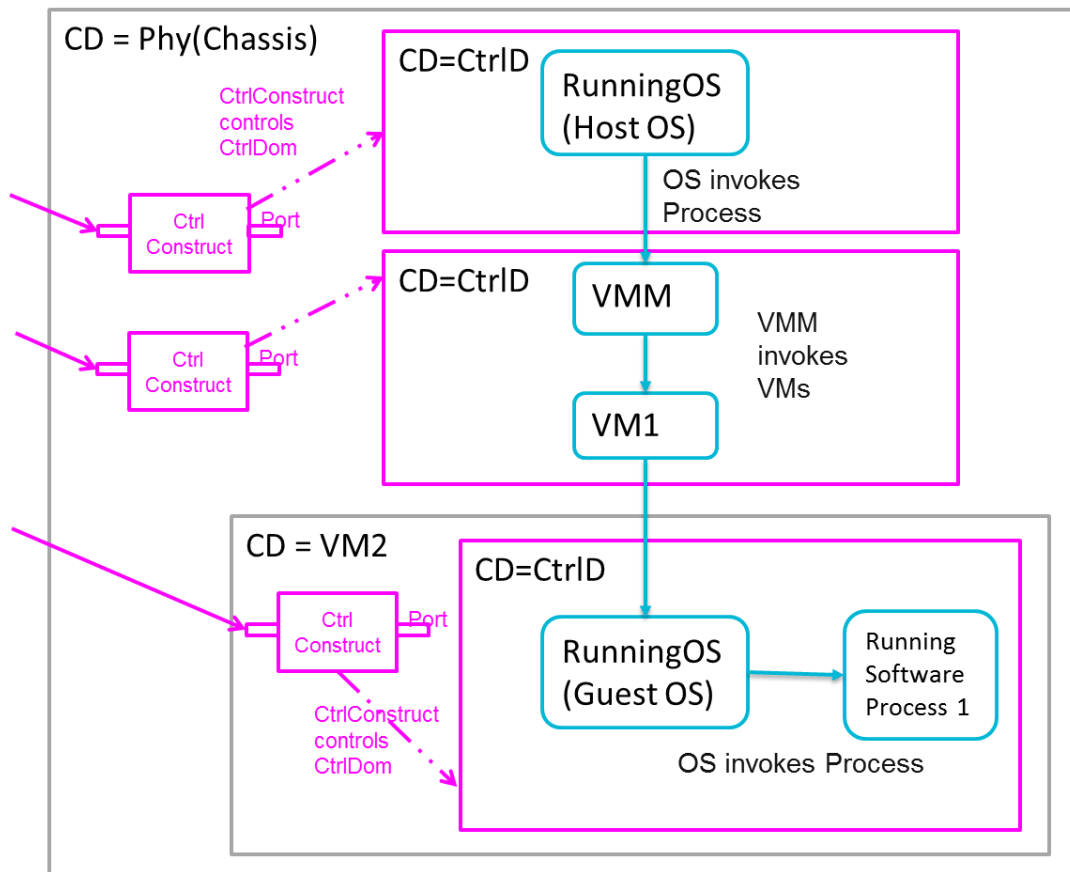


Figure 2-13 Considering control

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